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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/780,330	02/17/2004	Gilbert Wolrich	10559-127002/P7866C	1102
59796 INTEL CORP	7590 03/09/201 ORATION	EXAMINER		
c/o CPA Glob		THAMMAVONG, PRASITH		
P.O. BOX 520 MINNEAPOL	050 J.S. MN 55402	ART UNIT	PAPER NUMBER	
	,		2187	
			MAIL DATE	DELIVERY MODE
			03/09/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/780,330	WOLRICH ET AL.	
Examiner	Art Unit	
PRASITH THAMMAVONG	2187	

	PRASITH THAMMAVONG	2187				
The MAILING DATE of this communication appe	ars on the cover sheet with the o	orrespondence add	ress			
THE REPLY FILED 16 February 2010 FAILS TO PLACE THIS	APPLICATION IN CONDITION FO	R ALLOWANCE.				
 X The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following rapplication in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods: 	the same day as filing a Notice of a replies: (1) an amendment, affidavit al (with appeal fee) in compliance	Appeal. To avoid abar t, or other evidence, v with 37 CFR 41.31; or	hich places the (3) a Request			
a) The period for reply expiresmonths from the mailing b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire Is Examiner Note: If box 1 is checked, check either box (a) or (MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f)	dvisory Action, or (2) the date set forth ter than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE	date of the final rejection	n.			
Extensions of time may be obtained under 37 CFR 1,136(a). The date have been filed is the date for purposes of determining the period of extunder 37 CFR 1,17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1,704(b). NOTICE OF APPEAL	ension and the corresponding amount of hortened statutory period for reply origi	of the fee. The appropri- nally set in the final Office	ate extension fee e action; or (2) as			
 The Notice of Appeal was filed on A brief in complifiling the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed with the property of the property	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	s of the date of appeal. Since a			
AMENDMENTS						
 □ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because (a) □ They raise new issues that would require further consideration and/or search (see NOTE below); (b) □ They raise the issue of new matter (see NOTE below); (c) □ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for 						
appeal; and/or						
(d) ☐ They present additional claims without canceling a c NOTE: (See 37 CFR 1.116 and 41.33(a)).	orresponding number of finally reje	ected claims.				
4. The amendments are not in compliance with 37 CFR 1.12	11. San attached Nation of Nan Co.	mpliant Amandment (DTOL 224)			
 Applicant's reply has overcome the following rejection(s): 		ripliant Amendment (F TOL-324).			
Newly proposed or amended claim(s) would be all non-allowable claim(s).		imely filed amendmer	nt canceling the			
7. ∑ For purposes of appeal, the proposed amendment(s): a) I how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows: Claim(s) allowed:		be entered and an e	xplanation of			
AFFIDAVIT OR OTHER EVIDENCE						
 The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 						
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to or showing a good and sufficient reasons why it is necessary 	vercome <u>all</u> rejections under appea	l and/or appellant fail	s to provide a			
10. The affidavit or other evidence is entered. An explanation	of the status of the claims after er	ntry is below or attach	ed.			
XEQUEST FOR RECONSIDERATION/OTHER 1. ☑ The request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.						
12. Note the attached Information <i>Disclosure Statement</i> (s). (13. Other:	PTO/SB/08) Paper No(s).					
/P. T./	/Brian R. Peugh/					
Examiner, Art Unit 2187	Primary Examiner, Art U March 4, 2010	nit 2187				

Continuation of 11, does NOT place the application in condition for allowance because:

The Examiner acknowledges the applicant's submission of the amendment dated 2/16/10, which has been entered. At this point claims 28-35 and 44-45 have been cancelled. Thus, claims 36-43 are pending in the instant application.

In response to the argument that the Tremblay reference does not teach "mapping addresses in a single address space to resources within a set of multiple programmable units" where the single address space includes "addresses for different ones of the resources in different ones of the multiple programmable units", the Applicant's arguments have been fully considered but they are not persuasive. The Examiner contends that the Tremblay reference teaches the limitation above as it teaches the use of multiple registers within multiple processors (see fig. 3, registers 306 and 312, and processors p1 208 and 210) accessing locations within a shared instruction cache (see fig. 3, instruction cache 212) which allows accessors be the main memory 202, as seen in fig. 3, outurn 4, lines 7-29 and 30-58.

Also in response to the argument that the Tremblay reference does not teach a single address space, the Examiner contends that the Tremblay reference leaches the use of a single address space as the registers within the processors are accessing locations in the instruction cache 212 which allows access to the main memory 202, as noted in the citations above.

Thus, the Examiner contends that the Tremblay references teaches the limitations above as broadly and instantly claimed, and that the combination of the Tremblay and Sharma references teaches the limitations of claim 36 as broadly and instantly claimed.